

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A system for changing a bus configuration of a computing device, said system comprising:

a first bus of said computing device;

a second bus of said computing device;

a third bus coupled with a first device;

a fourth bus coupled with said first device;

a fifth bus coupled with a second device;

a sixth bus coupled with said second device;

a multiplexing module coupled with said first bus, said second bus, said third bus, said fourth bus, said fifth bus, and said sixth bus, and for selectively coupling said first and second busses with said third, fourth, fifth, or sixth busses; and

a configuration module coupled with said multiplexing module and for controlling operation of said multiplexing module.

2. (Currently Amended) The system of Claim 1 wherein said first device comprises a first controller adapter chip and said second device comprises a second controller adapter chip.

3. (Original) The system of Claim 2, further comprising:

a first add-in card slot coupled with said first controller adapter chip; and

a second add-in card slot coupled with said second controller adapter chip.

4. (Currently Amended) The system of Claim 1, wherein said first device comprises a first add-in card slot and said second device comprises a second add-in card slot.

5. (Original) The system of Claim 1, wherein said multiplexing module comprises electrical circuitry.

6. (Currently Amended) The system of Claim 1, wherein said multiplexing module comprises:

- a first configuration routing for coupling said first bus with said third bus;
- a second configuration routing for coupling said first bus with said fourth bus;
- a third configuration routing for coupling said first bus with said fifth bus;
- a fourth configuration routing for coupling said first bus with said sixth bus;
- a fifth configuration routing for coupling said second bus with said third bus;
- a sixth configuration routing for coupling said second bus with said fourth bus;
- a seventh configuration routing for coupling said second bus with said fifth bus; and
- an eighth configuration routing for coupling said second bus with said sixth bus.

7. (Original) The system of Claim 1, wherein said configuration module comprises a register.

8. (Original) The system of Claim 1, wherein said configuration module comprises a switch.

9. (Currently Amended) A method for changing a bus configuration of a computing device, said method comprising:

transmitting a first control signal to a configuration module;

causing a multiplexing module to couple a first bus with a second bus of said computing device, in response to said first control signal, said second bus coupled with a first device;

transmitting a second control signal to said configuration module, and

causing said multiplexing module to couple a third bus with a fourth bus of said computing device, in response to said second control signal, said fourth bus coupled with said first device.

10. (Currently Amended) The method as described in Claim 9, further comprising:

transmitting a third control signal to said configuration module; and

causing said multiplexing module to couple said first bus with a fifth bus in response to said third control signal, said fifth bus coupled with a second device.

11. (Original) The method as described in Claim 9, further comprising:

causing a controller adapter chip coupled with said second bus to go off-line.

12. (Original) The method as described in Claim 11, further comprising:

causing an add-in card to enter a sleep mode, said add-in card coupled with an add-in card slot which is coupled with said controller adapter chip.

13. (Original) The method as described in Claim 12, further comprising:
activating said controller adapter chip and said add-in card once said first bus is coupled with said second bus.

14. (Original) The method as described in Claim 9, further comprising:
causing an add-in card to enter a sleep mode, said add-in card coupled with an add-in card slot which is coupled with said second bus.

15. (Currently Amended) A system for modifying an input/output (I/O) bus configuration of a computer system, said system comprising:

- a first I/O bus of said computer system;
- a second I/O bus;
- a third I/O bus coupled with a first device;
- a fourth I/O bus coupled with said first device;
- a fifth I/O bus coupled with a second device;
- a sixth I/O bus coupled with said second device;
- a multiplexer circuitry coupled with said first I/O bus, said second I/O bus, said third I/O bus, said fourth I/O bus, said fifth I/O bus, and said sixth I/O bus, and for selectively coupling said first and second I/O busses with said third, fourth, fifth, or sixth I/O busses; and

a configuration module coupled with said multiplexer circuitry and for controlling operation of said multiplexer circuitry.

16. (Currently Amended) The system of Claim 15 wherein said first device comprises a first controller adapter chip and said second device comprises a second controller adapter chip.

17. (Original) The system of Claim 16, further comprising:
a first add-in card slot coupled with said first controller adapter chip; and
a second add-in card slot coupled with said second controller adapter chip.

18. (Currently Amended) The system of Claim 15 wherein said first device comprises a first add-in card slot and said second device comprises a second add-in card slot.

19. (Currently Amended) The system of Claim 15, wherein said multiplexer circuitry comprises:

a first configuration routing for coupling said first I/O bus with said third I/O bus;

a second configuration routing for coupling said first I/O bus with said fourth I/O bus;

a third configuration routing for coupling said first I/O bus with said fifth I/O bus;

a fourth configuration routing for coupling said first I/O bus with said sixth I/O bus;

a fifth configuration routing for coupling said second I/O bus with said third I/O bus;

a sixth configuration routing for coupling said second I/O bus with said fourth I/O bus;

a seventh configuration routing for coupling said second I/O bus with said fifth I/O bus; and

an eighth configuration routing for coupling said second I/O bus with said sixth I/O bus;.

20. (Original) The system of Claim 15, wherein said configuration module comprises a register.

21. (Original) The system of Claim 15, wherein said configuration module comprises a hardware switch.